Docket No.: GR 99 P 1058

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient ostage as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450,

tria, VA 22313-1450, on the date indicated below.

Date: August 8, 2003

## ATES PATENT AND TRADEMARK OFFICE

**Applicant** 

Wilfried Daehn

Applic. No.

09/484,781

Filed

January 18, 2000

Title

Integrated Semiconductor Circuit and Method for Functional

Testing of Pad Cells

Examiner

Don Le

Art Unit: 2819

## INFORMATION DISCLOSURE STATEMEN UNDER 37 C.F.R. 1.97(C)(1)

Hon. Commissioner for Patents

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

Japanese Patent Abstract JP 5 312 857 (Yasujima), dated November 26, 1993;

Japanese Patent Abstract JP 10 078 474 (Higuchi), dated March 24, 1998;

Korean Office Action dated June 24, 2003.

In accordance with 37 C.F.R. 1.97(e) the undersigned herewith states that each item of information contained in the information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications,

patents and/or publications, it is because no existing translation is readily available to the applicant.

In accordance with 37 C.F.R. 1.97 (c) (1), consideration of this Information Disclosure Statement is requested.

Respectfully submitted,

For Applicant

Date: August 8, 2003

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/bmb

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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))

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GR	aa	P	10	158	2	

Applic. No.

09/484,781

Applicant

Wilfried Daehn

Filing Date

Group Art Unit

January 18, 2000

2819

## **U.S. PATENT DOCUMENTS**

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
INITIALS		PATENTINO.	LDAIL	PAIENTEE	CLASS	CLASS	DAIL
	Α						
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	С						
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## FOREIGN PATENT DOCUMENT

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRAI YES	
J	5 312 857	11/26/93	Japan			Х	
К	10 078 474	03/24/98	Japan			Х	
L							
М							
N							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

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**EXAMINER** 

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.